

FILED UNDER 35 U.S.C. 371

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

10/088957

Best Available Copy

APPL NUM 10088957	FILING DATE 03/22/2002	CLASS 365	SUBCLASS 800	GAU 2818	EXAMINER KERVER
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\*\*APPLICANTS: Fey Wolfgang; Traskov Adrian; Truol Jan;

2/33

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/EP00/08398 08/29/2000

\*\* FOREIGN APPLICATIONS VERIFIED:

GERMANY 199 45 494.9 09/22/1999

GERMANY 100 18 22.6 04/15/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

AP9722

TITLE : Method and circuit configuration for storing data words in a ram module

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Primary Examiner	Application Examiner
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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